

## Patent Abstracts of Japan

EXPRESS MAIL NO. EV336616251US

PUBLICATION NUMBER : 58012025  
 PUBLICATION DATE : 24-01-83

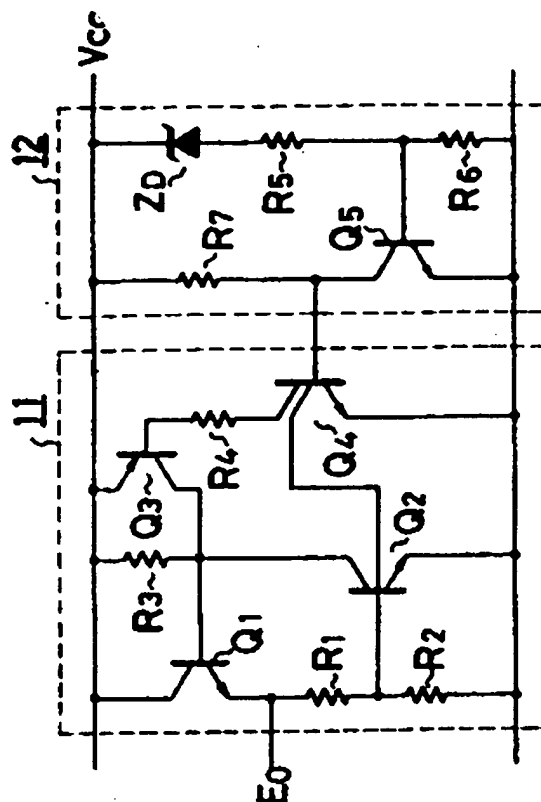
APPLICATION DATE : 15-07-81  
 APPLICATION NUMBER : 56110208

APPLICANT : TOSHIBA CORP;

INVENTOR : MIYAMOTO JUNICHI;

INT.CL. : G05F 1/56

TITLE : POWER SUPPLY CIRCUIT



ABSTRACT : PURPOSE: To improve the efficiency of power supply at low voltage, by controlling forward and reverse bias to a power supply circuit at high and low voltages respectively and setting the respective output states.

CONSTITUTION: When a power supply potential  $V_{cc}$  is low and a Zener diode  $ZD$  is nonconductive, a transistor (TR) $Q_5$  is turned off, a TR $Q_4$  turns on and the base of TRs  $Q_2$  and  $Q_3$  is grounded. Thus, the TR $Q_2$  is turned off and the TR $Q_3$  is turned on, and a current supplied from a power supply circuit can be controlled with a resistor  $R_4$ . When the power supply  $V_{cc}$  is high and the diode  $ZD$  is conductive, the TR $Q_3$  is turned off and the base current to the TR $Q_1$  is supplied from the power supply  $V_{cc}$  via a resistor  $R_2$ .

COPYRIGHT: (C)1983,JPO&Japio